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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10084102	FILING DATE 02/27/2002	CLASS 439	SUBCLASS ---	GAU 2833	EXAMINER <i>Leon</i>
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**CONTINUING DATA VERIFIED: *Noel RAL*

FOREIGN APPLICATIONS VERIFIED: *OK RAL*
GERMANY 101 00 571 6 02/28/2001

DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO P2001,0140
35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no	
Verified and Acknowledged Examiners's initials <i>hkl</i>	
TITLE : Printed circuit board configuration	

U.S. DEPT. OF COMM./PAT. & TM./PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
			Print Fig.
TERMINAL DISCLAIMER		Application Examiner	
PREPARED FOR ISSUE		WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368; Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.	

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